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STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner: National Semiconductor Corp.	oration
Application No./Patent No.: 10/016,750	Filed/Issue Date: December 10, 2001
Entitled: LEAD FRAME DESIGN FOR INCREASED CHIP PINOUT	
National Semiconductor Corporation (Name of Assignee)	. a Corporation (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)
states that it is: 1. $\boxed{y}$ the assignee of the entire right, title, and interest	est; or
an assignee of less than the entire right, title a     (The extent (by percentage) of its ownership in	and interest nterest is%)
in the patent application/patent identified above by vi	irtue of either:
A [7] An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Real 009125 Frame 0299, or for which a copy thereof is attached.	
OR  B. A chain of title from the inventor(s), of the pate	ent application/patent identified above, to the current assignee as follows:
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Additional documents in the chain of title ar	re listed on a supplemental sheet.
As required by 37 CFR 3.73(b)(1)(i), the docum assignee was, or concurrently is being, submitted for	nentary evidence of the chain of title from the original owner to the recordation pursuant to 37 CFR 3.11.
[NOTE: A separate copy (i.e., a true copy of the Division in accordance with 37 CFR Part 3, 302.08]	original assignment document(s)) must be submitted to Assignment to record the assignment in the records of the USPTO. See MPEP
The undersigned (whose title is supplied below) is au	uthorized to act on behalf of the assignee.
/Steve D Beyer/	May 9, 2008
Signature	Date
Steve D Beyer	(408) 255-8001
Printed or Typed Nam	ne Telephone Number
Attorney for Applicant (NSC1)	P226R)

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